

1 = 2 6874

PTO/SB/21 (08-00)_.
MODIFIED

TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Application Number	09/920,240
Filing Date	08/01/2001
First Named Inventor	Pierte ROO
Group Art Unit	2682
Examiner Name	Eugene Yun
Attorney Docket Number	MP0039.CIP

213200.00072			Examiner Name	Eug	gene Yun
Total Number o	f Pages in This Submission	25	Attorney Docket Numb	er MF	20039.CIP
		ENCL	OSURES (chec	k all th	nat apply)
	claration(s) dequest ent Request ure Statement with iority Re g Parts/	(for an A Drawing Licensin Petition Provisio Associal Termina Reques	nent Papers Application) (s) g-related Papers to Convert to a nal Application te Power of Attorney Il Disclaimer t for Refund mber of CD(s)	cor	After Allowance Communication to Group Appeal Communication to Board of Appeals and Interferences Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) Proprietary Information Status Letter Other Enclosure(s) (please identify below): MMUNICATION
	SIGNATURE (OF APPLI	CANT, ATTORNEY, O	AGE	NT
Firm or Individual name	Andrew J. Bateman Registration No.: 45	,573			
Signature		And	rew J. Batem	<u>~</u>	
Date		·	07/08/2005		
			ATE OF MAILING		
I hereby certify that this commail in an envelope address	respondence is being depos sed to: Commissioner for Pa	sited with thatents, Was	ne United States Postal Se shington, DC 20231 on this	rvice wit date:	th sufficient postage as first class

CERTIFICATE OF MAILING I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on this date: Typed or printed name Signature	
Typed or printed name	
Signature	Date

Complete if Known **EE TRANSMITTAL** 09/920,240 **Application Number** for FY 2005 08/01/2001 Filing Date Pierte ROO First Named Inventor Effective 10/01/2005. Patent fees are subject to annual revision. **Examiner Name** Eugene Yun Applicant claims small entity status. See 37 CFR 1.27 2682 Art Unit (\$) 180.00 TOTAL AMOUNT OF PAYMENT MP0039.CIP Attorney Docket No.

METH	OD OF PAY	MENT (check all that apply)					FEE	ECALCULATION (continued)	
Check	✓ Credit card	Money Other N	lone	3. A	DDIT	ONA	L FEE	S grade	
Denosit	Account:	Order L.		Large	Entity	Smal	I Entity	<u>.</u>	
Deposit Account	Account.	50-1710	7	Fe Code	9e \$	F Code	ee \$	<u>Description</u>	Fee Paid
Account			┥ᅦ	1051	130	2051	65	Surcharge - late filing fee or oath	
Name	KATTEN MU	JCHIN ROSENMAN, LLP]	1052	50	2052	25	Surcharge - late provisional filing fee or cover sheet	
The Directo	r is authorize	d to: (check all that apply)		1053	130	1053		Non-English specification	
Charge fee	e(s) indicated bel	low Credit any overpayme	ents	1812	•	1812	•	For filing a request for ex parte reexamination	
Charge a	bove Deposit A	Account with any additional fees		1804	920*	1804	920*	Requesting publication of SIR prior to Examiner action	
B .	-	FR 1.16 AND/0R 1.17 to maintain		1805	1,840*	1805	1,840*	Requesting publication of SIR after	
pendenc	y of this applic			4054	400	2254	60	Examiner action	
		ALCULATION		1251 1252	120	2251 2252	60 225	Extension for reply within first month Extension for reply within second month	<u> </u>
1. BASIC F				1252	450 1,020	2252	510	Extension for reply within third month	
Fee Entity	Small Entity Fee	Description Fee P	aid		1,590	2253	795	Extension for reply within fourth month	
Code (\$)	Code (\$)					1		Extension for reply within fifth month	
1001 300	2001 150	Utility Filing Fee 300.0	0		2,160		1,080		
1111 500	2111 250	Utility Search Fee 500.0		1401	500	2401	250	Notice of Appeal	
1311 200	2311 100	Patent Examination Fee 200.0	<u> </u>	1402	500	2402	250	Filing a brief in support of an appeal Request for oral hearing	
					1,000	2403	500	, J	
!					1,510	ı	1,510	Petition to institute a public use proceeding	
	s	SUBTOTAL (1) (\$) 0.00		1452	500	2452		Petition to revive - unavoidable (1.17(I))	
2. EXTRA	CLAIM FEES	S FOR UTILITY AND REIS	SUE	ŀ	1,500	2453		Petition to revive - unintentional (1.17(m))	
		Extra Claims Fee	Paid	•	1,400	2501 2502	700	· · · · ·	<u> </u>
Total Claims	- 20	r = x 50.00 =		1502	800 1,100	2502	400 550	<u> </u>	
Independent	- 3	·· = x 200.00 =		1460	130	1460	130		
Multiple Depe	endent	360.00 =		1807	50	1807	50	Processing fee - provisional app (1.17(q))	
Large Entity				1806	180	1806	180	Submission of Information Disclosure Stmt	180.00
Fee Code (\$)	Fee Code (\$)	<u>Description</u>		8021	40	8021	40	Recording each patent assignment per	
1202 50	2202 25	Claims in excess of 20						property (times number of properties)	
1201 200	2201 100	Independent claims in excess of	3	1809	790	2809	395	Filing a submission after final rejection (1.129(a))	
1203 360	2203 180	Multiple dependent claim, if not	paid	1814	130	2814	65	Statutory Disclaimer	
1204 200	2204 100	** Reissue independent claims over original patent		1801	790	2801	395	Request for Continued Examination (RCE)	
1205 50	2205 25	** Reissue claims in excess of 2 and over original patent	20	1802	900	1802		, , ,	
	CHE			Othe	r fee (s	pecify)			
** or number pre		BTOTAL (2) (\$) U.UU (STOTAL (2) (STOTAL (2		*Redu	ced by E	asic Fili	ng Fee P	Paid SUBTOTAL (3) (\$) 18(0.00
SUBMITTE	D BY								
<u> </u>	ndrew J. Ba	iteman	Doc-	istratio	n No	. 45	572	Telephone: (202) 625-35	47
	J. . J u	1	neg	isti dtl(m NO.		,,,,,,	(202) 020 00	
		1. 1. 1 R	1.						
Signature:		mound. D	w	wa	_			Date: 07/12/2005	



Application No. <u>09/920,240</u> Attorney's Docket No. <u>MP0039.CIP</u> Page 1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re Application of:)
) Examiner: Eugene YUN
Pierte ROO)
) Group Art Unit: 2682
Application No.: 09/920,240)
) Confirmation No.: 4035
Filed: August 1, 2001)
)
For: ACTIVE RESISTIVE SUMMER) Date: July 12, 2005
FOR A TRANSFORMER HYBRID)

COMMUNICATION PURSUANT TO 37 C.F.R. 1.56 AND M.P.E.P. § 2001.06(c)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In compliance with the requirements of 37 C.F.R. 1.56 and M.P.E.P. § 2001.06(c), the Applicant hereby notifies the Patent Office that the subject matter of the above-identified application may be involved in a litigation in the United States International Trade Commission, Investigation No. 337-TA-531.

Customer No.: 28285

Should the Examiner have any questions regarding this communication or the application in general, the Examiner is urged to contact the Applicant's attorney, Andrew J. Bateman, by telephone at (202) 625-3547. All correspondence should continue to be directed to the address given below.

Respectfully submitted,

B۷۰

Andrew J. Baterhan' Attorney for Applicant Registration No. 45,573

IP Docket Katten Muchin Rosenman LLP 1025 Thomas Jefferson St., NW East Lobby, Suite 700 Washington, DC 20007-5201 Facsimile No.: (202) 298-7570

Customer No.: 28285

Customer No.: 28285



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Pierte ROO) Examiner: Eugene YUN)
Application No.: 09/920,240) Group Art Unit: 2682
) Confirmation No.: 4035
Filed: August 1, 2001)
For: ACTIVE RESISTIVE SUMMER FOR) Date: July 12, 2005

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In compliance with the duty of disclosure under 37 CFR § 1.56 and in accordance with the practice under 37 CFR §§ 1.97 and 1.98, the Examiner's attention is directed to the documents listed on the enclosed PTO-1449. It is respectfully noted that Applicants do not have copies of certain literature and non-U.S. patent documents listed on the enclosed PTO-1449 forms. Applicants will provide copies of such to the Patent Office as soon as possible. If the Examiner has not received such copies at the time of consideration of the IDS, the Examiner is respectfully requested to contact the Applicants' undersigned attorney.

In accordance with 37 CFR § 1.97(h), this Information Disclosure Statement is not to be construed as an admission that the information cited is or is considered to be material to patentability as defined in 37 CFR § 1.56(b), nor as an admission that the information constitutes prior art within the meaning of 35 USC §§ 102 and/or 103.

It is respectfully requested that the information listed on the PTO-1449 be considered by the Examiner, and that an initialed copy of the PTO-1449 be returned indicating that such information was considered.

07/13/2005 SZEWDIE1 00000147 09920240

01 FC:1806

180.00 GP

Authorization for payment of the IDS fee of \$180.00 is being filed concurrently herewith.

Should the Examiner have any questions, Applicant's undersigned attorney is reachable by telephone in our Washington, D.C. office at (202) 625-3547. The correspondence address of record is provided below.

Respectfully submitted,

KATTEN MUCHIN ROSENMAN, LLP

Bv:

Andrew J. Bateman Registration No. 45,573

IP Docket Katten Muchin Rosenman, LLP 1025 Thomas Jefferson St., NW East Lobby, Suite 700 Washington, DC 20007-5201 Facsimile No.: (202) 298-7570

. ,		OIPE						
FORM PTO 1449 MODIF		2 2005				DOCKET NO.		ATION NO.
U.S. PATENT AND TRA	DEMARK	OFFICE JUL 1 2 2005 E			MP00	39.CIP		20,240
LIST OF B	FFE	ERENÇES CITEDSY	APPLICANT				LICANT	
		4.5	7.1.1.2.07.1111				te ROO	
DATE SUBMITTE	отоц	ISPTO: July 12, 2005				DATE		
					08/01	/2001	26	582
	TEN	T DOCUMENTS						·
*EXAMINER INITIALS		DOCUMENT NUMBER	DATE	COUN	TRY	CLASS	SUBCLASS	OR ABSTRACT
OTHER DOC	UME	NTS (Including author, tit	le, date, pertinen	t pages, etc.)				
		Stephens, "Active Outpu				vember, 200	2.	
		Hellwarth, et al., "Digital-	to-analog Conv	erter having	Common-m	ode Isolation	and Differer	ntial Output".
		Millman, et al., "Pulse, D	igital, and Switc	hing Wavefo	rms", pgs. 6	574-675.		
		Dally, et al., "Digital Sys	tems Engineerin	g", cover and	d pgs. 390-3	391.		
	-						 	
			<u>-</u>					
						-		
	-							
	╂							-
EXAMINER	<u> </u>		,	DATE CONSID	ERED			OUP 682 TRANSLATION OR ABSTRACT tial Output".
*EXAMINER: Initial if	reference	considered, whether or not citation is in conforma	nce with MPEP 609; Draw line th	rough citation if not in cor	iformance and not cons	idered. Include copy of th	is form with next communi	ication to applicant.

	35			ATTORNEY	DOCKET NO.	APPLICA	ATION NO.
FORM PTO 1449 MODIF U.S. PATENT AND TRA	DEMARK OFFICE JIL 1 2 2005 J		•		39.CIP		20,240
LIST OF R	EFERSACES CITES B	Y APPLICANT				PLICANT	
	COADENIES			FILIM	G DATE	te ROO	OUP
DATE SUBMITTEI	O TO USPTO: July 12, 2005				1/2001		682
FOREIGN PA	TENT DOCUMENTS						
*EXAMINER INITIALS	DOCUMENT NUMBER	DATE	COUN	ITRY	CLASS	SUBCLASS	TRANSLATION OR ABSTRACT

					<u> </u>		
					 		
OTHER DOC	I I UMENTS (Including author, t	itle date pertinent	names etc)		<u> </u>		<u></u>
OTTIETT BOO	Hamasaki, et al., "A 3-				with 100 dB I	Dynamic Ran	ge",
	December, 1996, pgs.		Dietal and D	· - ·	-1 0	011-	. 0 011
	Van de Plassche, "Inte 271.					·	r 6, pgs. 211-
	Lee, et al., " A CMOS	Serial Link for Fully	Duplexed I	Data Comm	nunication", A	pril, 1995.	
	Song, et al., "FP 12.1:		<u>.</u>	····			
	Song, et al., "FP 12.1:	NRZ Timing Reco	overy Techni	que for Ba	nd-Limited Cl	hannels (Slide	e
	Supplement), 1996.						
	Supplement), 1996. Chien, et al., "TP 12.4: for PCS Applications".	A 900-MHz Loca	l Oscillator u	ısing a DLL	based Frequ	uency Multipl	ier Technique
	Chien, et al., "TP 12.4:				•		ier Technique
	Chien, et al., "TP 12.4: for PCS Applications".	OS Frequency Syr	nthesizer for	Cellular Ap	oplications", N	March 12-13.	•
	Chien, et al., "TP 12.4: for PCS Applications". Chien, "Monolithic CM Chien, "Delay Based M	OS Frequency Syr Ionolithic CMOS F	nthesizer for requency S	Cellular Ap	oplications", N	March 12-13. Vireless Appl	ications", May
	Chien, et al., "TP 12.4: for PCS Applications". Chien, "Monolithic CM Chien, "Delay Based M 20, 1998. Chien, "Low-Noise Loc	OS Frequency Syr flonolithic CMOS F al Oscillator Desig , 2000.	nthesizer for requency S	Cellular Apynthesizer	oplications", Notes for Portable Volume	March 12-13. Wireless Appl quency Multi	ications", May
	Chien, et al., "TP 12.4: for PCS Applications". Chien, "Monolithic CMC Chien, "Delay Based N 20, 1998. Chien, "Low-Noise Loc Wireless Applications". Wang, et al., "A 1.2 GH	OS Frequency Syr Ionolithic CMOS F al Oscillator Desig 2000. Iz Programmable	nthesizer for requency S gn Technique DLL-Based	Cellular Apynthesizer the susing DI Frequency	oplications", Notes for Portable Volume L-based Fre	March 12-13. Wireless Appl quency Multi	ications", May
	Chien, et al., "TP 12.4: for PCS Applications". Chien, "Monolithic CM Chien, "Delay Based M 20, 1998. Chien, "Low-Noise Loc Wireless Applications". Wang, et al., "A 1.2 Gh December, 2004.	OS Frequency Syr flonolithic CMOS F al Oscillator Desig 2000. Iz Programmable 8-bit 2-ns Monolith	nthesizer for requency S gn Technique DLL-Based nic DAC", Fe	Cellular Apynthesizer the susing DI Frequency	oplications", Notes for Portable Volume L-based Free Multiplier for 388.	March 12-13. Wireless Appl quency Multi	ications", May
	Chien, et al., "TP 12.4: for PCS Applications". Chien, "Monolithic CM Chien, "Delay Based M 20, 1998. Chien, "Low-Noise Loc Wireless Applications". Wang, et al., "A 1.2 GH December, 2004. Tsutomu Kamoto, "An	OS Frequency Syr fonolithic CMOS F al Oscillator Desig , 2000. Iz Programmable 8-bit 2-ns Monolith nt Slew Rate Ethe	nthesizer for requency S gn Technique DLL-Based nic DAC", Fe	Cellular Apynthesizer the susing DI Frequency bruary, 198 iver", May,	oplications", Notes for Portable Volume L-based Free Multiplier for 38.	March 12-13. Wireless Appl quency Multi Wireless App	ications", May plier for plications",

ATTORNEY DOCKET NO. RADEMARK OFFICE MP0039.CIP APPLICANT LIST OF REFERENCES CITED BY APPLICANT Pierte ROO FILING DATE

APPLICATION NO.

09/920,240

GROUP

DATE SUBMITTED TO USPTO: July 12, 2005				08/01/2001		882
J.S. PATENT D	OCUMENTS		1 00/0	3112001		
*EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
-	6,177,896 B1	01/23/2001	Min			
	5,841,386	11/24/1998	Leduc		_	
	5,834,860	11/10/1998	Parsons, et al.			
"	5,838,186	11/17/1998	Inoue, et al.			
	5,243,347	09/07/1993	Jackson, et al.			
	5,283,582	02/01/1994	Krenik			
	5,589,788	12/31/1996	Goto			
	5,629,652	05/13/1997	Weiss			
	5,703,541	12/30/1997	Nakashima			
	5,719,515	02/17/1998	Danger			
	5,726,583	03/10/1998	Kaplinsky			
	6,046,607	04/04/2000	Kohdaka			
	6,087,968	07/11/2000	Roza			
	5,521,540	05/28/1996	Marbot			
	4,309,673	01/05/1982	Norberg, et al.		-	
	6,150,856	11/21/2000	Morzano			
	6,236,345 B1	05/22/2001	Dagnachew, et al.			
EXAMINER			DATE CONSIDERED			

^{*} EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered, Include copy of this form with next communication to applicant.

ATTORNEY DOCKET NO. APPLICATION NO. FORM PTO 1449 MANIFIED
U.S. PATENT AND PRADEMARK OFFICE 09/920,240 MP0039.CIP **APPLICANT** CIST OF REFERENCES CITED BY APPLICANT Pierte ROO FILING DATE GROUP DATE SUBMITTED TO USPTO: July 12, 2005 08/01/2001 2682 FOREIGN PATENT DOCUMENTS *EXAMINER **TRANSLATION** DOCUMENT NUMBER SUBCLASS DATE COUNTRY CLASS INITIALS **OR ABSTRACT** 63-300700 12/07/1988 Japan 06-97831 04/08/1994 Japan 06-97831 04/20/2005 Yes Japan 05-064231 A 03/12/1993 Japan 06-029853 02/04/1994 Japan 09-55770 08/17/1995 Japan 09-55770 08/17/1995 Japan Yes 09-270707 03/03/1996 Japan 09-270707 04/19/2005 Japan Yes 2001-177409 12/16/1999 Japan 2001-177409 04/20/2005 Japan Yes OTHER DOCUMENTS (Including author, title, date, pertinent pages, etc.) Johns, et al., "Integrated Circuits for Data Transmission Over Twisted Pair Channels", March, 1997, pgs. 398-406. "IEEE Standard 802.3: Part 3 Carrier Sense Multiple Access with Collision Detection (CSMA/CD) Access Method and Physical Detection", March 8, 2002, pgs. 1-378. Young, et al., "A Low-Noise RF Voltage-Controlled Oscillator Using On-Chip High-Q Three-Dimensional Coil Inductor and Micromachined Variable Capacitor", June 8-11, 1998, pgs. 128-131. Young, et al., "A Micromachined Variable Capacitor for Monolithic Low-Noise VCOS", 1996, pgs. 86-89. Abidi, et al., "FA 7.2: The Future of CMOS Wireless Transceivers", February 7, 1997, pgs. 118-119, 440. Eto, et al., "A 333 MHz, 20mW, 18ps Resolution Digital DLL using Current-controlled Delay with Parallel Variables Resistor DAC (PVR-DAC)", August 28-30, 2000, pgs. 349-350. Harald, et al., "Design of a 10-bit 100 MSamples/s BiCMOS D/A Converter", 1996, pgs. 730-733. Lee, et al., "A 3V 10b 100MS/s DIGITAL-TO-ANALOG CONVERTER FOR CABLE MODEM APPLICATIONS", August 28-30, 2000, pgs. 203-205. Henriques, et al., "A CMOS Steering-Current Multiplying Digital-to-Analog Converter", 1995, pgs. 145-Wikner, et al., "Modeling of CMOS Digital-to-Analog Converters for Telecommunication", May, 1999, Van der Plas, et al., "A 14-Bit Intrinsic Accuracy Q² Random Walk CMOS DAC", December, 1999, pgs. Radke, et al., "A 14-Bit Current-Mode ΣΔ DAC Based Upon Rotated Data Weighted Averaging", August, 2000, pgs. 1074-1084. Shui, et al., "Mismatch Shaping for a Current-Mode Multibit Delta-Sigma DAC", March, 1999, pgs. 331-338. **EXAMINER DATE CONSIDERED** EXAMINER: Initial if reterence considered, whether or not citation is in conformance with MPEP 609; Draw tine through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

JUL 1,2 2005 W

ATTORNEY DOCKET NO. APPLICATION NO. MP0039.CIP 09/920,240 **APPLICANT** LIST OF REFERENCES CITED BY APPLICANT Pierte ROO **FILING DATE** GROUP DATE SUBMITTED TO USPTO: July 12, 2005 08/01/2001 2682 **U.S. PATENT DOCUMENTS** *EXAMINER DOCUMENT NUMBER DATE NAME CLASS SUBCLASS FILING DATE INITIALS 6,259,957 B1 07/10/2001 Alexander, et al. 6,452,428 B1 09/17/2002 Mooney, et al. 5,745,564 04/28/1998 Meek 03/21/1995 5,399,996 Yates, et al. 5,887,059 03/23/1999 Xie, et al. Kondoh, et al. 5,185,538 02/09/1993 5,204,880 (A) 04/20/1993 Wurster, et al. 5,272,453 12/21/1993 Traynor, et al. 5,325,400 06/28/1994 Co, et al. 08/08/1995 Flannagan, et al. 5,440,514 5,440,515 08/08/1995 Chang, et al. 5,479,124 12/26/1995 Pun, et al. Zhang, et al. 5,559,476 09/24/1996 5,568,064 10/22/1996 Beers, et al. Winen 5,600,321 02/04/1997 11/11/1997 Gist, et al. 5,687,330 5,757,298 (A) 05/26/1998 Manley, et al. Runaldue, et al. 5,798,661 08/25/1998 5,838,177 (A) 11/17/1998 Keeth Wohlfarth, et al. 5,999,044 12/07/1999 6,052,076 04/18/2000 Patton, III, et al. 6,057,716 05/02/2000 Dinteman, et al. 6,166,572 12/26/2000 Yamoaka 6,721,379 B1 04/13/2004 Cranford, Jr., et al. 5,859,552 01/12/1999 Do, et al. **EXAMINER DATE CONSIDERED**

^{*} EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered, include copy of this form with next communication to applicant.

FORM PTO 1449 MODIFIED	MARK OFFICE				DOCKET NO.		ATION NO.
U.S. PATENT AND TRADE	<u></u>		···	MP00	39.CIP		20,240
LIST OF RE	Ferences Street by	APPLICANT				PLICANT	
				FII INC	DATE	te ROO	OUP
DATE SUBMITTED 1	TO USPTO: July 12, 2005				/2001		382
FOREIGN PAT	ENT DOCUMENTS			00,0.	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,		
*EXAMINER INITIALS	DOCUMENT NUMBER	DATE	COUN	TRY	CLASS	SUBCLASS	TRANSLATION OR ABSTRACT
OTHER DOCU	MENTS (Including author, ti						
	Weaver, Jr., "A Third M 1956, pp. 1703-1705.						
	Niknejad et al., "Analysi pp. 375-378.	s and Optimization	on of Monolit	hic Inductor	s and Transf	formers for R	F ICs," 1997,
	Weigandt et al., "Analys	sis of Timing Jitte	rs in CMOS I	Ring Oscilla	tors," pp. 27	-30.	
	Niknejad et al., "Analysi IC's," October 1998, pp		ptimization o	f Spiral Indu	uctors and T	ransformers f	or Si RF
	Gray et al., Analysis an		og Integrated	Circuits, Fo	ourth Edition	•	
	American National Stan Medium Dependent (TF				DDI) – Toke	n Ring Twiste	ed Pair Layer
	Nguyen et al., "Si IC-Co				s," August 1	990, pp. 1028	3-1031.
	Gardner, "Charge-Pum	p Phase-Lock Lo	ops," Novem	ber 1980, p	p. 1849-185	8.	
	Dally et al., "High Perfo	rmance Electrica	l Signaling."				
	Davies, "Digital Genera	tion of Low-Freq	uency Sine W	/aves," June	e 1969, pp. 9	97-105.	
	Abidi, "TP 11.1: Direct-0	Conversion Radio	o Transceive	s for Digital	Communica	ations," 1995.	
	Dolle, "A Dynamic Line-						
	Su et al., "Experimental Integrated Circuits," Ap			iques for Su	ıbstrate Nois	e in Mixed-Si	ignal
	Gray et al., "Future Dire	ections in Silicon	ICs for RF Pe	ersonal Con	nmunications	s," 1995, pp. 8	33-90.
	Gabara, "On-Chip Term	ninating Registers	s for High Sp	eed ECL-Cl	VIOS Interfac	ces," 1992, pp	. 292-295.
	Horowitz et al., "High-S	peed Electrical S	signaling: Ove	erview and L	imitations,"	1998, pp. 12-	24.
						· · · · · · · · · · · · · · · · · · ·	
		 	-				
							· · · · · · · · · · · · · · · · · · ·
EXAMINER			DATE CONSID	ERED			

\0... \alpha \forall

* EXAMINER: trialal if reference considered, whether or not citation is in conformance with MPEP 609; Draw Ene through citation if not in conformance and not considered, include copy of this form with next communication to appEcant.

FORM PTO 1449 MODELE	ED .				EY DOCKET NO.	APPLIC	ATION NO
U.S. PATENT AND TOTAL	MARK OFFICE			MPC	0039.CIP		20,240
MENOF RE	ED MARK OFFICE EFERENCES CITED BY TO USPTO: July 12, 2005	Y APPLICANT	1			PLICANT	
				FIL	.ING DATE	te ROO	ROUP
DATE SUBMITTED ?	TO USPTO: July 12, 2005		1		01/2001		682
FOREIGN PAT	TENT DOCUMENTS						
*EXAMINER INITIALS	DOCUMENT NUMBER	DATE	COUN	∤TRY	CLASS	SUBCLASS	TRANS OR ABS
OTHER DOCU	IMENTS (Including author, ti				ircuit in 2-µm (MOS," 1990	, pp. 13
	1394. Liu et al., "WP 23.7: A 6 484.	6.5 GHz Monolithi	ic CMOS Vol	Itage-Con	trolled Oscillat	or," 1999, pp	. 404-40
	Wang et al., "WP 23.8:	A 9.8 GHz Back-	Gate Tuned	VCO in 0	.35 µm CMOS,	," 1999, pp. 4	06-407
	Rofougaran et al., "SP						
	Koullias et al., "TP 9.2: Terminals," 1993, pp. 1	140-141, 278.					
	Dauphinee et al., "SP 2 Resonator," 1997, pp. 3	390-391, 491.					grated
	Banu et al., "A BiCMOS Chang et al., "A CMOS						1996 r
	63. Waizman, "FA 18.5: A l						
	1994, pp. 298-299. Kinget, "FP 14.7: A Full	•	•				
	February 5, 1998. Lee et al., "A Fully Integ	grated Low-Noise	1-GHz Freq	quency Sy		• • • • • • • • • • • • • • • • • • • •	
	Communication Applica Parker et al., "A Low-N				Loop Filter," 1	997, pp. 407	. 409-4
	Park et al., "A Low-Nois						
	Soyuer et al., "A Monol Technology," Decembe	er 1993, pp. 1310-	-1313.				
	Hu et al., "A Monolithic December 1993, pp. 13	314-1320.				·	
	Parameswaran et al., ", 6, 1998, pp. 289-307.						
ļ	Cho et al., "TP 13.5: A Spectrum Digital Cordle				Fransceiver tor	900 MHz Sp 	read-
				<u></u>		<u></u>	

(1) -1 2: 71115 (4) FORM PTO 1449 MODIFER				ATTORNI	EY DOCKET NO.	APPLICA	ATION NO.		
FORM PTO 1449 MODIFIED U.S. PATENT AND TRESPE	MARK OFFICE	-		MPO	0039.CIP	09/92	20,240		
MOSMATE RE	FERENCES CITED BY	APPLICANT				LICANT			
						e ROO			
DATE SUBMITTED T	ΓΟ USPTO: July 12, 2005				ING DATE		OUP		
EODEICH DAT	ENT DOCUMENTS			08/	01/2001	20	582		
*EXAMINER	·					OLINETOV OLABO			TRANSLAT
INITIALS	DOCUMENT NUMBER	DATE	COUN	ITRY	CLASS	SUBCLASS	OR ABST		
				_					
OTHER DOCU	MENTS (Including author, titl								
	Sedra et al., Microelectro	onic Circuits, Third	d Edition, 19	991, pp. 8	86-92.				
	Moon et al., "An All Anald	og Multiphase De	lay Locked	Loop Usi	ng a Replica D	elay Line for	Wide Ra		
	Operation and Low-Jitter								
	I.E.E.E. Standard 802.3: Access Method and Phys					Detection (C	SMA/CI		
	Shoval et al., "WA 18.7 -					r with Progra	mmable		
	Performance/Power Fea	tures," 2000, pp.	314-315.						
	Myson Technology, "MTI		Encoder/De	ecoder an	d 10BaseT Tra	nsceiver with	Built-in		
	Waveform Shaper," 1997 Myson Technology, "MTI		v) 100Base	TX PCS/F	PMA." 1997. pp	1-21			
	Craninckx et al., "A 1.8-0 1997, pp. 736-744.	Hz Low-Phase-N	Noise CMO	S VCO U	sing Optimized	Hollow Spira	al Inducto		
	Craninckx et al., "A 1.8-0	GHz Low-Phase-N	Noise CMO	S VCO U	sing Optimized	Hollow Spira	al Inducto		
	1995, pp. 1474-1482.				· · · · · · · · · · · · · · · · · · ·				
	Hung et al., "A 1.24-GHz 1999, pp. 111-113.	Monolithic CMO	S VCO with	Phase N	loise of 137 dB	c/Hz at a 3-N	/IHz Offs		
	Rudell et al., "A 1.9-GHz	Wide-Band IF De	ouble Conv	ersion CN	/IOS Receiver f	or Cordless	Telephor		
	Applications," 1997, pp. 2	2071-2088.					•		
	Lin et al., "TP 12.5: A 1.4 PLL Architecture," 2000,			CMOS F	requency Synt	hesizer using	a Wide		
				lad Ossille	ntor" 1007 no	200 200			
	Razavi, "SP 23.6: A 1.8								
	Dec et al., "MP 4.8: A 1.9 449.	9 GHZ MICROMACI	nine-Based	Low-Pna	se-Noise CMO	S VCO," 199	9, pp. 80		
	Sato et al., "SP 21.2: A 1	.9 GHz Single-Cl	hip IF Trans	sceiver fo	r Digital Cordle	ss Phones,"	February		
	1996.					· · · · · · · · · · · · · · · · · · ·			
	Rudell et al., "SA 18.3: A Cordless Telephone App				ersion CMOS Ir	ntegrated Re	ceiver fo		
	Lee et al., "A 2.5 V CMO	S Delay-Locked	Loop for an	18 Mbit,	500 Megabytes	s/s DRAM," 1	994, pp.		
	1491-1496.		·						
	Leong et al., "A 2.7-V 90 Communication," 1999,		Dual-Band	Transceiv	er IC for Digital	Wireless			
	Lam et al., "WP 23.6: A		CMOS Vol	tage-Con	trolled Oscillato	r." 1999. pp.	402-403		
	484.								
	Marshall et al., "TA 8.7:	A 2.7V GSM Tran	nsceiver ICs	s with On-	Chip Filtering,"	1995.			
EXAMINER		Ti	DATE CONSID	ERED					
* FXAMINER: Initial if refe	erence considered, whether or not citation is in conformar	are with MPEP 609. Draw fine throu	und orientia delle	oformance and not	noncidered brokeds some of the	e form with part commun	·		

FORM PTO 1449 MENTIED U.S. PATENT AND PRADEMARK OFFICE			MP0039.CIP		APPLICATION NO. 09/920,240						
LIST OF REFERENCES CITED BY APPLICANT			APPLICANT 09/920,240								
List of references cited by applicant						te ROO					
DATE SUBMITTED TO USPTO: July 12, 2005			FILING DATE		GROUP						
EOREICH DA	TENT DOCUMENTS			08/01	/2001	20	582				
*EXAMINER		DATE	0011	TDV	01.400	011001 400	TRANSLATION				
INITIALS	DOCUMENT NUMBER DATE			TRY	CLASS	SUBCLASS	OR ABSTRACT				
	62-159925	7/15/87	-	JP							
OTHER ROOM	6-276182	9/30/94	JF	<u></u>	-						
OTHER DOC	UMENTS (Including author, tit Sedra et al., Microelectr			 	<u></u>						
	Yee et al., An Integratat 1999	Yee et al., An Integratable 1-2.5 Gbps Low Jitter CMOS Transceiver with Built in Self Test Capability, 1999									
	Intersil, HC-5509B ITU (Intersil, HC-5509B ITU CO/Loop Carrier SLIC, 8/2003									
	Regan, ADSL Line Drive	er/Receiver Desi	gn Guide, Pa	rt 1, 2/2000	1						
	Phillps, The HC-5502X1	Phillps, The HC-5502X14X Telephone Subscriber Line Interface Circuits (SLIC), 1/1997									
	Fuad Et al., An Operation	Fuad Et al., An Operational Amplifier Circulator Based on the Weighted Summer, 6/1975									
	Narayanan et al., Doppl	Narayanan et al., Doppler Estimation Using a Coherent Ultrawide-Band Random Noise Radar, 6/2000									
	Stephens, Active Outpu	Stephens, Active Output Impedance for ADLS Line Drivers, 11/2002									
	High Speed Modem Sol	High Speed Modem Solutions Info Card 20									
	Hellums et al., An ADSL	Hellums et al., An ADSL Integrated Active Hybrid Circuit									
	Everitt et al., A CMOS T	Everitt et al., A CMOS Transceiver for 10-Mb/s and 100-Mb/s Ethernet, 12/1998									
	Azadet et al., A Gigabit 2/2000	Azadet et al., A Gigabit Transceiver Chip Set for UTP CA-6 Cables in Digital CMOS Technology, 2/2000									
	He et al., A DSP Receiv	He et al., A DSP Receiver for 1000 Base-T PHY, 2001									
	Baird et al., A Mixed Sa	Baird et al., A Mixed Sample 120M s PRML Solution for DVD Systems, 1999									
	Baker, An Adaptive Cab	Baker, An Adaptive Cable Equalizer for Serial Digital Rates to 400Mb/s, 1996									
	Chan et al., A 100 Mb/s						<u> </u>				
	Everitt et al., A 10/100N 1998	Everitt et al., A 10/100Mb/s CMOS Ethernet Transceiver for 10BaseT, 10BaseTX and 100Base FX, 1998									
	Kelly et al., A Mixed Sig	nal DFE/Ffe Red	ceiver for 100	BaseTX Ap	plications, 2	000					
	Shoaei et al., A 3V Low	Power 0.25um (CMOS 100M	o/s Receive	r for Fast Etl	nernet, 2000					
	Walker et al., A Two Ch	ip 1.5 GBd Seria	I Link Interfa	ce, 12/1992							
	Linear Technology High	Speed Modem	Solutions Info	o Card							
EVANGINED											

^{*} EXAMINER: Initial if reterence considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

DATE CONSIDERED

EXAMINER



/いった /

FORM PTO 1449 MODIFIED U.S. PATENT AND TRADEMARK OFFICE		ATTORNEY DOCKET NO.	APPLICATION NO.			
		MP0039.CIP	09/920,240			
LIST OF REFERENCES CITED BY APPLICANT		APPLICANT				
eigi gi iibi biibiyaba giibb gi Ai i biaAiyi			e ROO			
DATE SUBMITTED TO USPTO: July 12, 2005		FILING DATE	GROUP			
AGUER BAAMMENDA (L. L. L	,	08/01/2001	2682			
OTHER DOCUMENTS (Including author, title, date, pertinent						
Mueller, Combining Echo Cancellation and	d Decision F	-eedback Equalization, ()2/29/1979			
Roo et al., A CMOS Transceiver Analog F	ront-end for	Gigabit Ethernet over C	at-5 Cables, 2001			
Shoval, A Combined 10/125 Mbaud Twist Features, 2000	ed Pair Line	Driver with Programma	ble Performance/Power			
Knight, Jr. et al., A Self-Terminating Low-	Voltage Swi	ng CMOS Output Driver	, 1988, 457-464			
Maneatis, Low-Jitter Process-Independen 1723-1732	it DLL and P	LL Based on Self-Biase	d Techniques, 11/1996,			
Chang et al., Large Suspended Inductors 5/1993, 246-248	on Silicon a	nd Their Use in a 1-um	CMOS RF Amplifier,			
	Gharpurey et al., Modeling and Analysis of Substrate Coupling in Integrated Cicuits, 3/1996, 344-353					
Young et al., Monolithic High-Performance Communications, 1997	Young et al., Monolithic High-Performance three-Dimensional Coil Inductors for Wireless Communications, 1997					
Efendovich et al., Multifrequency Zero-Jitt	Efendovich et al., Multifrequency Zero-Jitter Delay-Locked Loop, 1/1994, 67-70					
· · ·	Munshi et al., Adaptive Impedance Matching, 69-72					
Niknejad et al., Numerically Stable Green Integrated Circuits, 4/1998, 305-315	Niknejad et al., Numerically Stable Green Function for Modeling and Analysis fo Substrate Coupling in Integrated Circuits, 4/1998, 305-315					
Hajimiri et al., Phase Noise in Multi-Gigah	Hajimiri et al., Phase Noise in Multi-Gigahertz CMOS Ring Oscillators, 1998, 49-52					
	Kim et al., PLL/DLL System Noise Analysis for Low Jitter Clock Synthesizer Design, 31-34					
Rudell et al., Recent Developments in Hig Communication Systems, 1998, 149-154	Rudell et al., Recent Developments in High Integration Multi-Standard CMOS Transceivers for Personal Communication Systems, 1998, 149-154					
	Shoval et al., A 100 Mb/s BiCMOS Adaptive Pulse-Shaping Filter, 12/1995, 1692-1702					
	Jansen et al., SP 23.8: Silicon Bipolar VCO Family for 1.1 to 2.2 GHz with Fully-Integrated Tank and Tuning Circuits, 2/8/1997, 392-393 & 492					
	··					
EXAMINER	DATE CONSIDE	RED				

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation it not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO 1449 MODIFIED
U.S. PATENT AND TRADEMARK OFFICE

OF REFERE ATTORNEY DOCKET NO. APPLICATION NO. MP0039.CIP 09/920,240 APPLICANT LIST OF REFERENCES CITED BY APPLICANT Pierte ROO **FILING DATE** GROUP DATE SUBMITTED TO USPTO: July 12, 2005 08/01/2001 2682

IS DATENT	DOCUMENTS			1/2001	20	02
*EXAMINER	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
INITIALS						1
	3,973,089	8/3/1976	Puckett			
	4,131,767	12/26/78	Weinstein			
	4,321,753	3/30/82	Fusari			
	4,535,206	8/13/85	Falconer			
	4,621,356	11/04/1986	Scipione			
	4,715,064	11/22/87	Claessen			
	4,727,566	2/23/88	Dahlqvist			
	4,817,081	3/28/89	Wouda et al.			
	4,878,244	10/31/89	Gawargy, M.			
	4,888,762	12/19/89	Arai			
	4,894,820	1/16/90	Miyamoto			
	4,970,715	11/13/90	McMahan			
	4,993,045	2/12/91	Alfonso			
	5,018,134	5/21/91	Kokubo et al.			
	5,119,365	6/2/92	Warner et al.			
	5,148,427	9/15/92	Buttle et al.			
	5,243,346	9/7/93	Inami			
	5,245,654	9/14/93	Wilkison et al.			
	5,248,956	9/28/93	Himes			
	5,253,249	11/12/93	Fitzgerald et al.			
	5,280,526	1/18/94	Laturell			
	5,282,157	1/25/94	Murphy et al.			
	5,365,935	11/22/94	Righter et al.			
	5,367,540	11/22/94	Kakushi et al.			
	5,465,272	11/7/95	Smith			
EXAMINER			DATE CONSIDERED			

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered, include copy of this form with next communication to applicant.

JUL 1 2 2005

FORM P10 1449 MODIFIED

ATTORNEY DOCKET NO. APPLICATION NO. U.S. PATENT AND TRADEMARK OFFICE MP0039.CIP 09/920,240 APPLICANT LIST OF REFERENCES CITED BY APPLICANT Pierte ROO **FILING DATE GROUP** DATE SUBMITTED TO USPTO: July 12, 2005 08/01/2001 2682 **U.S. PATENT DOCUMENTS** *EXAMINER DOCUMENT NUMBER DATE NAME CLASS SUBCLASS FILING DATE INITIALS 5,507,036 4/9/96 Vagher 5,557,027 11/19/96 Cheng 5,613,233 3/18/97 Vagher 9/22/98 5,812,597 Graham et al. 5,841,809 Koizumi et al. 11/24/98 5,887,059 3/23/99 Xie et al. 5,894,496 4/13/99 Jones 7/27/99 Devline et al. 5,930,686 6,005,370 12/21/99 Gustavson 3/14/00 6,038,266 Lee et al. 6,192,226 2/20/01 Fang 6,266,367 7/24/01 Strait 6,259,680 7/10/01 Blackwell et al. 6,259,957 7/10/01 Alexander et al. 4/23/02 Dobson et al. 6,377,683 6,389,077 5/14/02 Chan 6,408,032 6/18/02 Lye et al. 6,163,579 12/19/00 Harrington et al. 7/10/01 Blackwell et al. 6,259,680 6,731,748 5/4/04 Edgar et al. 6,744,831 6/1/04 Chan 6,751,202 6/15/04 Henrie 2002-0009057 6/24/02 Blackwell et al. Bion et al. 2003-0174660 9/18/03 7/22/97 5,651,029 Yang et al. **EXAMINER DATE CONSIDERED**

^{*} EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw time through citation if not in conformance and not considered, include copy of this form with next communication to applicant.

JUL 1,2 2005

U.S. PATENT AND TRADEMARK OFFICE ATTORNEY DOCKET NO. APPLICATION NO. MP0039.CIP 09/920,240 APPLICANT LIST OF REFERENCES CITED BY APPLICANT Pierte ROO FILING DATE **GROUP** DATE SUBMITTED TO USPTO: July 12, 2005 08/01/2001 2682 **U.S. PATENT DOCUMENTS** *EXAMINER DOCUMENT NUMBER DATE NAME CLASS **SUBCLASS** FILING DATE INITIALS 1/9/01 6,173,019 Hee et al. 6,223,061 4/24/01 Dacus et al. 6,236,645 5/22/01 Agazzi 6,259,957 7/10/01 Alexander et al. 6,298,046 10/2/01 Thiele Cook et al. 6,421,534 7/16/02 6,823,028 11/23/04 Phanse 6,043,766 3/28/00 Hee et al. 6,044,489 3/29/00 Hee et al. 5,269,313 12/14/93 **DiPinto** 5,880,615 3/9/99 **Bazes** 6,140,857 10/31/00 Bazes 6,148,025 11/14/00 Shirani et al. Nguyen et al. 6,211,716 4/3/01 6,385,238 5/7/02 Nguyen et al. 6,408,032 6/18/02 Lye et al. 6,415,003 7/2/02 Raghaven 4,621,172 11/04/86 Kanemasa et al. 6,370,190 4/9/02 Young et al. 3/13/01 Agazzi et al. 6,201,831 6,377,640 4/23/02 Trans 5,579,004 11/26/96 Linz 5,577,027 11/19/96 Cheng 5,365,935 11/22/94 Righter et al. 6,049,706 4/11/00 Cook et al. 6,731,748 5/4/04 Edgar, III et al. **EXAMINER** DATE CONSIDERED * EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered, include copy of this form with next communication to applicant.

FORM PTO 144 SEPTIFIED U.S. PATENT OF TRADEMARK OFFICE LIST OF REFERENCES CITED BY APPLICANT				Y DOCKET NO.		ATION NO.			
			MP0039.CIP		09/920,240				
			APPLICANT						
	I EILENGES ONED DI	ALLUAN				te ROO			
DATE SUBMITTED TO USPTO: July 12, 2005			FILING DATE 08/01/2001		GROUP 2682				
							FOREIGN PAT	ENT DOCUMENTS	<u></u>
*EXAMINER INITIALS	DOCUMENT NUMBER	DATE	COUN	COUNTRY		SUBCLASS	TRANSLATION OR ABSTRACT		
							-		
i	-		•			180 1 0 40000			
			<u> </u>						
OTHER DOCUI	 MENTS (Including author, ti	l tle, date, pertinen	t pages, etc.)	1					
	Falconer; "Echo Cance 08/13/1985	llation in Two Wir	e Full Duple:		mation of Far	-End Data Co	omponents";		
	Gawargy; "Electronic H	Gawargy; "Electronic Hybrid Circuit"; 10/31/1989							
	Cho et al.; "A Single-Ch Cordless Telephones";		Conversion	Transceive	er for 900 MHz	Spread-Spe	ctrum Digital		
	Shoval et al.; "A CMOS	S Mixed-Signal 10	00Mb/s Rece	eive Archite	ecture for Fast	t Ethernet"; 1	999		
	Hester et al.; "CODEC	for Echo-Canceli	ng Full-Rate	ADSL Mo	dems"; Dece	mber, 1999			

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw fine through citation if not in conformance and not considered, include copy of this form with next communication to applicant.

DATE CONSIDERED

EXAMINER

JUL 1, 2 2005

U.S. PATENT AND TRADEMARK OFFICE MP0039.CIP 09/920,240 APPLICANT LIST OF REFERENCES CITED BY APPLICANT Pierte ROO **FILING DATE GROUP** DATE SUBMITTED TO USPTO: July 12, 2005 08/01/2001 2682 **U.S. PATENT DOCUMENTS** *EXAMINER DOCUMENT NUMBER DATE NAME CLASS SUBCLASS FILING DATE INITIALS 6,163,579 12/19/2000 Harrington et al. 4,621,172 11/04/1986 Kanemasa et al. 5.841.809 11/24/1988 Koizumi et al. 5,018,134 05/21/1991 Kobuku et al. Laturell 5,280,526 01/18/1994 11/13/1990 McMahon 4,970,715 01/25/1994 Murphy et al. 5,282,157 12/26/1978 4,131,767 Weinstein 09/14/1993 Wilkison et al. 5,245,654 Wouda et al. 4,817,081 03/28/1989 5,887,059 03/23/1999 Xie et al. 10/08/2002 6,462,688 Sutardja 3,543,009 11/24/1970 Voelcker 3.297,951 01/10/1967 Blasbalg 4,071,842 01/31/1978 Tewksbury 4,309,673 01/05/1982 Norberg et al. 4,408,190 10/04/1983 Nagano 4,464,545 08/07/1984 Werner 5,403,421 03/05/1985 Hareyama 4,527,126 07/02/1985 Petrich et al. 05/27/1986 4,591,832 Fling et al. 4,605,826 08/12/1986 Kanemasa 4,626,803 12/02/1986 Holm 4,816,830 03/28/1989 Cooper 4,868,571 09/19/1989 Inamasu **EXAMINER DATE CONSIDERED** EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

ATTORNEY DOCKET NO.

APPLICATION NO.

JUL- 1 2 COMPTO 14 STODIFIED PROPERTY OF REFERE ATTORNEY DOCKET NO. APPLICATION NO. MP0039.CIP 09/920,240 APPLICANT LIST OF REFERENCES CITED BY APPLICANT Pierte ROO **FILING DATE** GROUP DATE SUBMITTED TO USPTO: July 12, 2005 08/01/2001 2682 **U.S. PATENT DOCUMENTS** *EXAMINER DOCUMENT NUMBER DATE NAME CLASS **SUBCLASS FILING DATE** INITIALS 4,972,360 11/20/1990 Cukier et al. 4,988,960 01/29/1991 Tomisawa Koike 5,084,865 01/28/1992 Yousefi-Elezei 5,136,260 08/04/1992 5,212,659 05/18/1993 Scott et al. 09/14/1993 Kocis et al. 5,245,231 5,253,272 10/12/1993 Jaeger et al. 5,307,064 04/26/1994 Kudoh 5,307,405 04/26/1994 Sih 5,323,157 06/21/1994 Ledzius et al. 10/18/1994 5,357,145 Segaram 5,375,147 12/20/1994 Awata et al. 5,388,123 02/07/1995 Uesugi et al. 5,392,042 02/21/1995 Pellon Uesegi et al. 5,444,739 08/22/1995 05/14/1996 5,517,435 Sugiyama 5,537,113 07/16/1996 Kawabata 5,539,403 07/23/1996 Tani et al. 5,539,773 07/23/1996 Knee et al. 10/22/1996 Velazquez et al. 5,568,142 5,579,004 11/26/1996 Linz 5,651,029 07/22/1997 Yang et al. Koizumi et al. 5,659,609 08/19/1997 Galton 5,684,482 11/04/1997 Poklemba 5,696,796 12/09/1997 **EXAMINER DATE CONSIDERED**

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw the through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO 1445 SOUTHED U.S. PATENT OF TRADEMARK OFFICE ATTORNEY DOCKET NO. APPLICATION NO. MP0039.CIP 09/920,240 APPLICANT LIST OF REFERENCES CITED BY APPLICANT Pierte ROO **FILING DATE GROUP** DATE SUBMITTED TO USPTO: July 12, 2005 08/01/2001 2682 **U.S. PATENT DOCUMENTS** *EXAMINER DOCUMENT NUMBER DATE NAME CLASS SUBCLASS FILING DATE INITIALS 5,963,069 10/05/1999 Jefferson et al. 5,982,317 11/09/1999 Steensgaard-Madsen 6,014,048 01/11/2000 Talaga et al. 6,037,812 03/14/2000 Gaudet 04/04/2000 Lau et al. 6,047,346 6,067,327 05/23/2000 Creigh et al. 6,094,082 07/25/2000 Gaudet 6,100,830 08/08/2000 Dedic 6,137,328 11/24/2000 Sung 11/21/2000 6,150,856 Morzano 6,172,634 B1 Leonowich et al. 01/09/2001 6,201,490 B1 03/13/2001 Kawano et al. 6,215,429 B1 04/10/2001 Fischer et al. 6,236,345 B1 05/22/2001 Dagnachew et al. 6,249,249 B1 06/19/2001 Obayashi et al. 6,259,745 B1 07/10/2001 Chan 6,271,782 B1 08/07/2001 Steensgaard-Madsen 6,289,068 B1 09/11/2001 Hassoun et al. Lindfors et al. 6,313,775 B1 11/06/2001 6,333,959 B1 12/25/2001 Lai et al. 6,339,390 B1 01/15/2002 Velazquez et al. 6,340,940 B1 01/22/2002 Melanson 6,351,229 B1 02/26/2002 Wang 6,373,417 B1 04/16/2002 Melanson 6,385,442 B1 05/07/2002 Vu et al. **EXAMINER DATE CONSIDERED**

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw Ene through citation if not in conformance and not considered, Include copy of this form with next communication to applicant.

FORM PTO 1449 MODIFIED
U.S. PATENT AND PRADEMARK OFFICE
TOF REFERE ATTORNEY DOCKET NO. APPLICATION NO. MP0039.CIP 09/920,240 **APPLICANT** LIST OF REFERENCES CITED BY APPLICANT Pierte ROO GROUP **FILING DATE** DATE SUBMITTED TO USPTO: July 12, 2005 08/01/2001 2682 **U.S. PATENT DOCUMENTS** *EXAMINER DOCUMENT NUMBER NAME CLASS **SUBCLASS** DATE **FILING DATE** INITIALS 6,421,377 B1 07/16/2002 Langberg et al. 08/27/2002 6,441,761 B1 Viswanathan 6,476,749 B1 11/05/2002 Yeap et al. 12/10/2002 6,492,922 B1 New 6,509,857 B1 01/21/2003 Nakao 6,531,973 B2 03/11/2003 Brooks et al. 6,539,072 B1 03/25/2003 Donnelly et al. 6,570,931 B1 05/27/2003 Song Tanaka 6,714,825 B1 03/30/2004 6,816,097 B2 11/09/2004 Brooks et al. Sutardja et al. 6,844,837 B1 01/18/2005 Williams 2002-0061087 A1 05/23/2002 2002-0084857 A1 07/04/2002 Kim 2004-0141569 A1 07/22/2004 Agazzi 5,243,346 09/07/1993 Inami Shih et al. 5,267,269 11/30/1993 11/28/2000 Liu 6,154,784 6,163,283 12/19/2000 Schofield 6,163,289 12/19/2000 Ginetti 6,185,263 B1 02/06/2001 Chan

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Bult et al.

Chan

Greig

Litfin et al.

Cranford Jr. et al.

DATE CONSIDERED

02/20/2001

06/19/2001

07/10/2001

09/25/2001

10/23/2001

6,191,719 B1

6,249,164 B1

6,259,745 B1

6,259,012 B1

6,037,490 B1

EXAMINER

FORM PTO 1449 MODIFIED
U.S. PATENT AND TO DEMARK OFFICE

FORM PTO 1449 MODIFIED
U.S. PATENT AND TO DEMARK OFFICE

MP0039.CIP

O9/920,240

APPLICANT
Pierte ROO

FILING DATE
GROUP

O8/01/2001

2682

U.S. PATENT DOCUMENTS

EXAMINER INITIALS

DOCUMENT NUMBER

DATE

O0440 0000 Pt 10 office of the state of th

*EXAMINER INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	6,346,899 B1	02/12/2002	Hadidi	<u> </u>		
	6,369,734 B2	04/09/2002	Volk			
	6,389,077 B1	05/14/2002	Chan			
	6,501,402 B2	12/31/2002	Boxho			
	6,509,854 B1	01/21/2003	Morita et al.			
	5,949,362	09/07/1999	Tesche et al.			
	5,373,147	12/20/1994	Awata et al.			
	5,790,060	08/04/1998	Tesche			
	60/106,265	10/30/1998	Chan			
	60/107,105	11/04/1998	Chan			
	60/107,702	11/09/1998	Chan			
	60/108,001	11/11/1998	Chan			l
	6,563,870	05/13/2003	Schenk			
	6,583,742	06/24/2003	Hossak			
	6,608,743	08/19/2003	Suzuki			
	6,332,004	12/18/2003	Chan			
	6,690,742	02/10/2004	Chan			
	5,164,725	11/17/1992	Long			
	6,259,745	07/10/2001	Chan			
WALLING .			DATE CONCIDENCE			

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

DATE CONSIDERED

EXAMINER